



JUN 04 2002

~~Intraoperative application of:~~

DAVID D. WU
MICHAEL P. DUANE
SCOTT D. LUNING

Serial No.: 09/784,629

Filed: February 15, 2001

For: SEMICONDUCTOR DEVICE AND
METHOD FOR LOWERING MILLER
CAPACITANCE FOR HIGH-SPEED
MICROPROCESSORS

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**RESPONSE TO RESTRICTION
REQUIREMENT DATED MAY 2, 2002**

**Assistant Commissioner for Patents
Washington, D.C. 20231**

CERTIFICATE OF MAILING

This paper is submitted in response to the Restriction Requirement dated May 2, 2002, for which the one-month date for response is June 2, 2002.

In response to the restriction requirement which the Examiner imposed, Applicant elects, with traverse, to prosecute claims 1-20, *i.e.*, the Group I claims.

It is believed that no fee is due; however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason relating to this document, the Assistant Commissioner is authorized to deduct said fees from Advanced Micro Devices, Inc. Deposit Account No. 01-0365/TT3633. In the event the monies in that account are insufficient, the Assistant Commissioner is authorized to

withdraw funds from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.032100.

The Examiner is invited to contact the undersigned attorney at (713) 934-4055 with any questions, comments or suggestions relating to the referenced patent application.

Respectfully submitted,



Date: May 20, 2002

J. Mike Amerson
Reg. No. 35,426
WILLIAMS, MORGAN & AMERSON, P.C.
7676 Hillmont, Suite 250
Houston, Texas 77040
(713) 934-4055
(713) 934-7011 (facsimile)

ATTORNEY FOR APPLICANTS